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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of: **HORII, Sadayoshi, et al.**

Group Art Unit: 1765

Serial No.: 10/528,450

Examiner: **Kin Chan Chen**

Filed: **May 22, 2006**

**P.T.O. Confirmation No.: 4305**

**FOR: METHOD FOR MANUFACTURING SEMICONDUCTOR DEVICE AND  
SUBSTRATE PROCESSING APPARATUS**

**REQUEST FOR STATUS**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Date: January 10, 2007

Sir:

The undersigned attorney requests the status of the above-identified application.

The application was filed on **May 22, 2006**, in the U.S. Patent and Trademark Office and  
to date no Office Action has been received.

In the event any fees are required in connection with this paper, please charge Deposit  
Account No. 01-2340.

Respectfully submitted,

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PATENT TRADEMARK OFFICE